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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/982,953	10/22/2001	Allen McTeer	M4065.0247/P247-A	8778

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EXAMINER
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KENNEDY, JENNIFER M

ART UNIT	PAPER NUMBER
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2812

DATE MAILED: 11/04/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

09/982,953

Applicant(s)

MCTEER, ALLEN

Examiner

Jennifer M. Kennedy

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 12 October 2004.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 22-35 and 58 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 22-35, 58 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

**DETAILED ACTION**

***Claim Rejections - 35 USC § 112***

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 30, 31, and 35 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 30 recites the limitation "said conductor" in line 3. There is insufficient antecedent basis for this limitation in the claim.

Claim 31 recites the limitation "said conductor " in line 2. There is insufficient antecedent basis for this limitation in the claim.

Claim 35 recites the limitation "said conductor" in line 1. There is insufficient antecedent basis for this limitation in the claim.

Applicant has amended independent claim 29 from which claims 30, 31, and 35 depend to claim a first and second conductive plug to replace the recitation of conductor. The examiner cannot ascertain which conductive plug is being referred to when "conductor" is being recited.

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and

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the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 22-35 and 58 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chiang et al. (U.S. Patent No. 5,739,579) in view of Moslehi et al. (U.S. Patent No. 6,016,000).

In re claim 22, Chiang et al. disclose the method of forming a copper interconnect structure providing electrical connection for a semiconductor device (see column 5, lines 25-31, and column 12, line 64 through column 13, line 5), comprising the steps of;

forming a first contact opening into a first insulating layer (350) formed over a semiconductor substrate (320);

forming a conductive plug in the first contact opening (361);

forming a second insulating layer (391) over the conductive plug and said first insulating layer;

forming a second contact opening in the second insulating layer (391);

forming a barrier layer (393) in the second contact opening;

forming a copper conductor (394) over the barrier layer; and

forming a top etch stop/heat radiating layer (392) on an upper surface portion of said copper conductor (394), said top heat-radiating layer passivating said upper surface portion of said copper conductor, wherein said top heat-radiating layer is formed from approximately 100 angstroms to approximately 1000 angstroms (see specifically column 15, lines 16-25, and column 20, lines 24-33, the method explained in detail with reference to the lower interconnect layer, the details given in column 12, line 53, through column 20, line 24).

Chiang et al. does not disclose the method of forming the etching stop/heat-radiating passivation layer of aluminum nitride. Moslehi discloses the method of forming an etch stop/heat radiating passivation layer of aluminum nitride (see column 13, lines 32-38, and lines 55-60).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to form the eth stop layer of aluminum nitride, since as Moslehi teaches AlN is an alternative choice to that of the silicon nitride etch stop layer formed in Chiang et al. Further, Moslehi teaches AlN has the advantage of high thermal conductivity (see column 12, lines 32-50, and column 14, lines 16-60). The examiner notes that while both Chiang et al. and Moslehi teach that the silicon nitride or aluminum nitride is an etch stop layer, the heat-radiating effect of silicon nitride and aluminum nitride is an intrinsic material property of the aluminum nitride (see Moslehi column 12, lines 32-50, and column 14, lines 48-60), and thus the aluminum nitride layer of Moslehi as incorporated into Chiang et al. would have acted as both a etch stop layer and a heat radiating layer.

In re claim 23, Chiang et al. also disclose the method of chemical mechanical polishing (CMPing) the copper layer and the barrier layer (see column 20, lines 1-3).

In re claim 24, Chiang et al. does not disclose the method of cleaning the upper surface portion of the copper conductor prior to the formation of the aluminum nitride layer. Moslehi discloses the method of cleaning the upper surface portion of the copper conductor prior to the formation of the aluminum nitride layer (see column 12, lines 32-35). It would have been obvious to one of ordinary skill in the art at the time the

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invention was made to clean the upper surface portion of the copper conductor prior to the formation of the aluminum nitride layer in order to remove contaminants from the surface.

In re claim 25, the combined Chiang et al. and Moslehi disclose the method wherein the aluminum nitride is formed to a thickness of approximately 300 angstroms (see Chiang et al. column 15, lines 15-25)

In re claim 26 and 27, Moslehi discloses the method of forming the aluminum nitride layer by sputtering deposition (see column 13, lines 15-17, and column 14, lines 45-46 for Moslehi definition of PVD).

In re claim 28, Chiang discloses the method wherein the barrier layer is formed of a refractory metal compound being selected from the group consisting of refractory metal nitrides, refractory metal carbides, and refractory metal borides (see column 19, lines 4-10).

In re claim 29, Chiang et al. disclose the method of forming an interconnect structure providing electrical connection for a semiconductor device (see column 5, lines 25-31, and column 12, line 64 through column 13, line 5), comprising the steps of;

forming a contact opening in an insulating layer (350) of said device;

forming a first conductive plug (361) within said contact opening; and

forming a etch stop/heat radiating layer (390) on an upper surface portion of said first conductive plug, said etch stop/heat radiating layer providing a heat dissipating path for said first conductive plug, wherein said heat radiating layer is formed form

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approximately 100 to approximately 1000 angstroms thick and depositing a second conductive plug on said heat-radiating layer (see specifically column 15, lines 16-25, and column 20, lines 24-33, the method explained in detail with reference to the lower interconnect layer, the details given in column 12, line 53, through column 20, line 24).

Chiang et al. does not disclose the method of forming the etching stop/heat-radiating passivation layer of aluminum nitride. Moslehi discloses the method of forming an etch stop/heat radiating passivation layer of aluminum nitride (see column 13, lines 32-38, and lines 55-60).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to form the eth stop layer of aluminum nitride, since as Moslehi teaches AlN is an alternative choice to that of the silicon nitride etch stop layer formed in Chiang et al. Further, Moslehi teaches AlN has the advantage of high thermal conductivity (see column 12, lines 32-50, and column 14, lines 16-60). The examiner notes that while both Chiang et al. and Moslehi teach that the silicon nitride or aluminum nitride is an etch stop layer, the heat-radiating effect of silicon nitride and aluminum nitride is an intrinsic material property of the aluminum nitride (see Moslehi column 12, lines 32-50, and column 14, lines 48-60), and thus the aluminum nitride layer of Moslehi as incorporated into Chiang et al. would have acted as both a etch stop layer and a heat radiating layer.

In re claim 30, Chiang et al. also disclose the method of forming a barrier layer (360) in said contact opening and before said step of depositing said conductor.

In re claim 31, Chiang et al. does not disclose the method of cleaning the upper surface portion of the copper conductor prior to the formation of the aluminum nitride layer. Moslehi discloses the method of cleaning the upper surface portion of the copper conductor prior to the formation of the aluminum nitride layer (see column 12, lines 32-35). It would have been obvious to one of ordinary skill in the art at the time the invention was made to clean the upper surface portion of the copper conductor prior to the formation of the aluminum nitride layer in order to remove contaminants from the surface.

In re claim 32, the combined Chiang et al. and Moslehi disclose the method wherein the aluminum nitride is formed to a thickness of approximately 300 angstroms (see Chiang et al. column 15, lines 15-25)

In re claim 33 and 34, Moslehi discloses the method of forming the aluminum nitride layer by sputtering deposition (see column 13, lines 15-17, and column 14, lines 45-46 for Moslehi definition of PVD).

In re claim 35, Chiang discloses the method wherein the barrier layer is formed of a refractory metal compound being selected from the group consisting of refractory metal nitrides, refractory metal carbides, and refractory metal borides (see column 19, lines 4-10). In re claim 31, Moslehi discloses the method of cleaning the upper surface portion of the copper conductor prior to the formation of the aluminum nitride layer (see column 12, lines 32-35) in order to remove contaminants from the surface.



In re claim 58, Chiang et al. disclose the method of forming a copper interconnect structure providing electrical connection for a semiconductor device (see column 5, lines 25-31, and column 12, line 64 through column 13, line 5), comprising the steps of;

forming a first contact opening into a first insulating layer (322) formed over a semiconductor substrate (320);

forming a conductive plug in the first contact opening (342);

forming a second insulating layer (350) over the conductive plug and said first insulating layer;

forming a second contact opening in the second insulating layer;

forming a barrier layer (360) in the second contact opening;

forming a first copper conductor plug (361) over the barrier layer; and

forming a etch stop/heat radiating layer (390) on an upper surface portion of said first copper conductor plug, said heat radiating layer passifying said upper surface portion of said first copper conductor plug; and

forming a second copper conductor plug (394) over said first copper conductor plug in contact with said heat-radiating layer (see specifically column 15, lines 16-25, and column 20, lines 24-33, the method explained in detail with reference to the lower interconnect layer, the details given in column 12, line 53, through column 20, line 24).

Chiang et al. does not disclose the method of forming the etching stop/heat-radiating passivation layer of aluminum nitride. Moslehi discloses the method of forming an etch stop/heat radiating passivation layer of aluminum nitride (see column 13, lines 32-38, and lines 55-60).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to form the etch stop layer of aluminum nitride, since as Moslehi teaches AlN is an alternative choice to that of the silicon nitride etch stop layer formed in Chiang et al. Further, Moslehi teaches AlN has the advantage of high thermal conductivity (see column 12, lines 32-50, and column 14, lines 16-60). The examiner notes that while both Chiang et al. and Moslehi teach that the silicon nitride or aluminum nitride is an etch stop layer, the heat-radiating effect of silicon nitride and aluminum nitride is an intrinsic material property of the aluminum nitride (see Moslehi column 12, lines 32-50, and column 14, lines 48-60), and thus the aluminum nitride layer of Moslehi as incorporated into Chiang et al. would have acted as both an etch stop layer and a heat radiating layer.

### ***Response to Arguments***

Applicant's arguments with respect to claims 22-35 and 58 have been considered but are moot in view of the new ground(s) of rejection.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer M. Kennedy whose telephone number is (571) 272-1672. The examiner can normally be reached on Mon.-Fri. 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Niebling can be reached on (571) 272-1679. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

  
Jennifer M. Kennedy  
Patent Examiner  
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jmk